

PRIOR ART

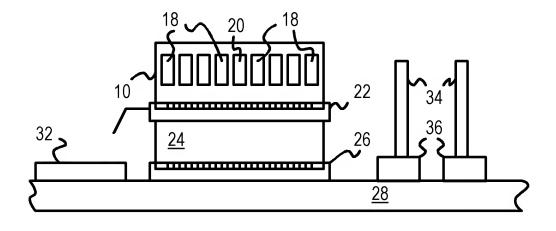
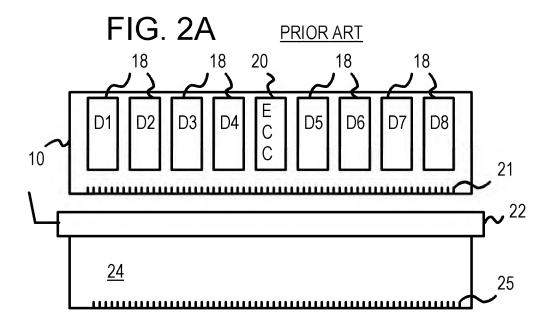


FIG. 1B

PRIOR ART



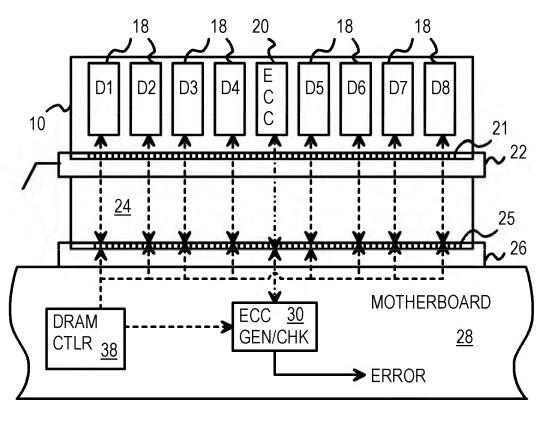


FIG. 2B

PRIOR ART

	ECC DRAM	DATA DRAMS					
	ECC .	D4	D3	D2	D1		
	0011	1010	1010	1010	1010		
FIG. 3	0001	0101	0101	0101	0101		
	0011	1010	1010	1010	1010		
<u>PRIOR ART</u>	0001	0101	0101	0101	0101		

	DRAN	ΛI			
D1	D2	D3	D4	ECC	
1010	1010	1010	0011	1010	
0101	0101	0101	0001	0101	
1010	1010	1010	0011	1010	
0101	0101	0101	0001	0101	
	1010 0101 1010	D1 D2 1010 1010 0101 0101 1010 1010	1010 1010 1010 0101 0101 0101 1010 1010 1010	D1 D2 D3 D4 1010 1010 1010 0011 0101 0101 0101 0	DATA DRAMS DRAMS D1 D2 D3 D4 ECC 1010 1010 1010 0011 1010 0101 0101

FIG. 6

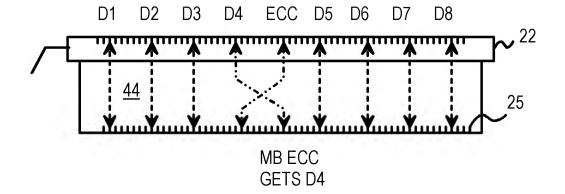


FIG. 4

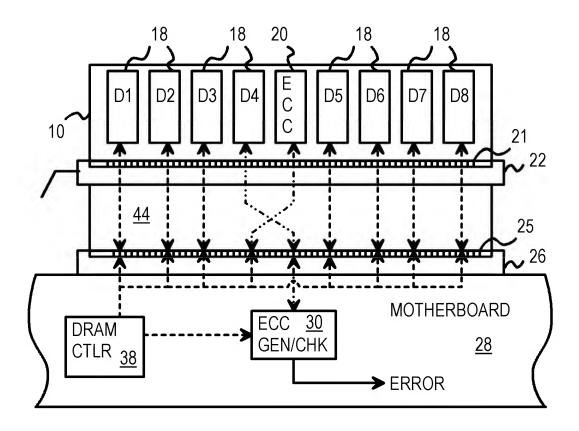
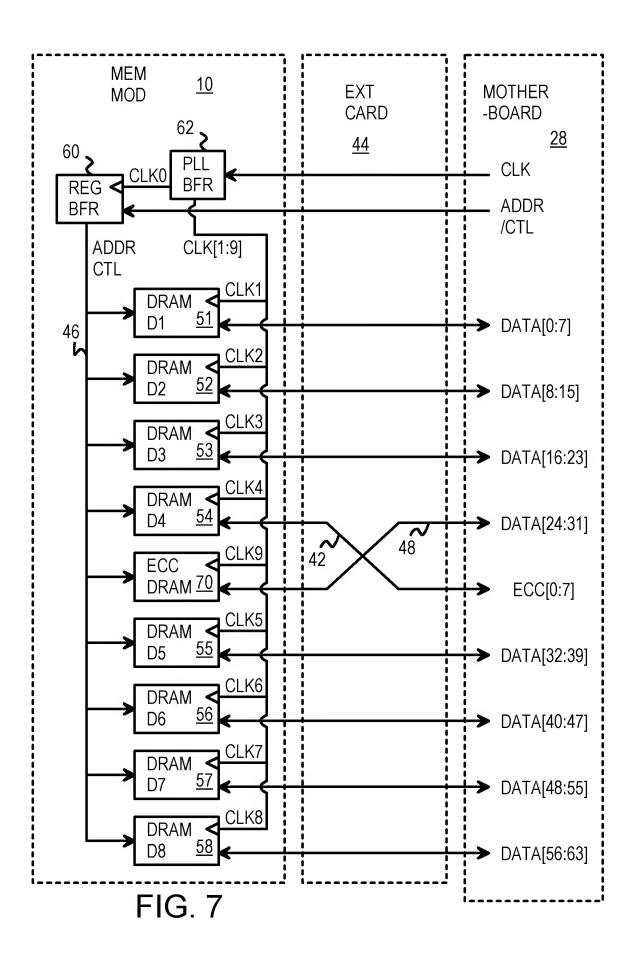


FIG. 5



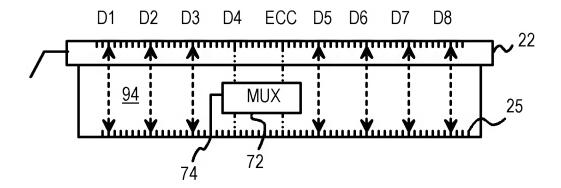


FIG. 8